



	VIA	PIA	
0	PB	} PA	<b>BOARD DECODE</b> BLOCK X, PAGE Y, LINK Z, (0,4,8,C) ADDRESS OF CHIPS XYZ0 TO XY(Z+3)0 E.G. WITH SWITCHES SET AS SHOWN ADDRESSES ARE :-  IC4 IC3 IC2 IC1 6F00 6F10 6F20 6F30
1	PA		
2	ODRB		
3	DDRA	} CRA	
4	T1L		
5	T1H		
6	T1L		
7	T1H	} PB	
8	T2L		
9	T2H		
A	SR	} CRB	
B	ACR		
C	PCR		
D	IFR		
E	IER		
F	PA - NO HANDSHAKE		

**CU-BIO**  
4xVIA or 4xPIA

ISSUE	COMMENT	DATE	DM
1			
2			
3			
4			
5			

10330

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